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Result page: [1](#) [2](#) [next](#)**1** [A survey of rollback-recovery protocols in message-passing systems](#)
[E. N. \(Mootaz\) Elnozahy, Lorenzo Alvisi, Yi-Min Wang, David B. Johnson](#)
September 2002 **Computing Surveys (CSUR)**, Volume 34 Issue 3**Publisher:** ACM
 Full text available: Pdf (549.68 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), in [terms](#), [review](#)
Bibliometrics: Downloads (6 Weeks): 83, Downloads (12 Months): 610, Citation Count: 84

This survey covers rollback-recovery techniques that do not require special language constructs. In the first part of the survey we classify rollback-recovery protocols into *checkpoint-based* and *log-based*. *Checkpoint-based* protocols ...

Keywords: message logging, rollback-recovery**2** [Trace-driven memory simulation: a survey](#)
[Richard A. Uhlig, Trevor N. Mudge](#)
June 1997 **Computing Surveys (CSUR)**, Volume 29 Issue 2**Publisher:** ACM
 Full text available: Pdf (636.11 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), in [terms](#), [review](#)
Bibliometrics: Downloads (6 Weeks): 46, Downloads (12 Months): 234, Citation Count: 45


As the gap between processor and memory speeds continues to widen, methods for evaluating memory system designs before they are implemented in hardware are becoming increasingly important. One such method, trace-driven memory simulation been the ...

Keywords: TLBs, caches, memory management, memory simulation, trace-driven simulation**3** [A selective, just-in-time aspect weaver](#)
[Yoshiki Sato, Shigeru Chiba, Michiaki Tatsubori](#)
September 2003 **GPCE '03: Proceedings of the 2nd international conference on Generative programming and component engineering****Publisher:** Springer-Verlag New York, Inc.
 Full text available: Pdf (256.62 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), in [terms](#)
Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 48, Citation Count: 8

Dynamic AOP (Aspect-Oriented Programming) is receiving growing interests in both academia and the industry. Since it allows weaving aspects with a program at runtime


is useful for rapid prototyping and adaptive software. However, the previous ...

4 [MarieSim: The MARIE computer simulator](#)

 Linda Null, Julia Lobur

June 2003 **Journal on Educational Resources in Computing (JERIC)** , Volume 3 Issue 2

Publisher: ACM


Full text available:  Pdf (340.79 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [in terms](#)

Bibliometrics: Downloads (6 Weeks): 23, Downloads (12 Months): 240, Citation Count: 2

MarieSim is a computer architecture simulator based on the MARIE architecture and designed to teach beginning computer organization and architecture. It provides user with interactive tools and simulations to help them deepen their understanding of ...

Keywords: Computer architecture simulator, education, introductory architecture

5 [Relative debugging: a new methodology for debugging scientific applications](#)

 David Abramson, Ian Foster, John Michalakes, Rok Sosič

November 1996 **Communications of the ACM** , Volume 39 Issue 11

Publisher: ACM

Full text available:  Pdf (462.99 KB) Additional Information: [full citation](#), [references](#), [cited by](#), [index term](#)


Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 33, Citation Count: 9

6 [Visualization challenges for a new cyberpharmaceutical computing paradigm](#)

Russell J. Turner, Kabir Chaturvedi, Nathan J. Edwards, Daniel Fasulo, Aaron L. Halpern, Daniel H. Huson, Oliver Kohlbacher, Jason R. Miller, Knut Reinert, Karin A. Remington, Russell Schwartz, Brian Walenz, Shibu Yooseph, Sorin Istrail

October 2001 **PVG '01: Proceedings of the IEEE 2001 symposium on parallel and large-d visualization and graphics**


Publisher: IEEE Press

Full text available:  Pdf (3.07 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index term](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 36, Citation Count: 0


In recent years, an explosion in data has been profoundly changing the field of biology and creating the need for new areas of expertise, particularly in the handling of data. A vital area that has so far received insufficient attention is how to communicate ...

7 [A thread-aware debugger with an open interface](#)

 Daniel Schulz, Frank Mueller

August 2000 **ISSTA '00: Proceedings of the 2000 ACM SIGSOFT international symposium on Software testing and analysis**

Publisher: ACM

Full text available:  Pdf (347.13 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [in terms](#)

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 27, Citation Count: 2


While threads have become an accepted and standardized model for expressing concurrency and exploiting parallelism for the shared-memory model, debugging threads is still poorly supported. This paper identifies challenges in debugging threads and offers ...

Keywords: active debugging, concurrency, debugging, open interface, threads

Also published in:


September 2000 **SIGSOFT Software Engineering Notes** Volume 25 Issue 5

8 [Helper threads via virtual multithreading on an experimental itanium® 2 processor based platform](#)

 Perry H. Wang, Jamison D. Collins, Hong Wang, Dongkeun Kim, Bill Greene, Kai-Ming Chao, Amir B. Yunus, Terry Sych, Stephen F. Moore, John P. Shen

December 2004 **ASPLOS-XI: Proceedings of the 11th international conference on Architectural support for programming languages and operating systems**

Publisher: ACM

Full text available:  Pdf (225.47 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [in terms](#)

Bibliometrics: Downloads (6 Weeks): 10, Downloads (12 Months): 120, Citation Count: 8

Helper threading is a technology to accelerate a program by exploiting a processor's multithreading capability to run "assist" threads. Previous experiments on hyper-threaded processors have demonstrated significant speedups by using helper threads

Keywords: DB2 database, PAL, cache miss prefetching, helper thread, itanium processor multithreading, switch-on-event


Also published in:

November 2004 **SIGPLAN Notices** Volume 39 Issue 11

December 2004 **SIGOPS Operating Systems Review** Volume 38 Issue 5


December 2004 **SIGARCH Computer Architecture News** Volume 32 Issue 5

9 [Using generative programming to visualise hypercode in complex and dynamic systems](#)

 Katherine Micken, Ron Morrison, Graham Kirby, Dharini Balasubramaniam, Evangelos Ziris

January 2004 **ACSC '04: Proceedings of the 27th Australasian conference on Computer science - Volume 26**, Volume 26

Publisher: Australian Computer Society, Inc.


Full text available:  Pdf (524.92 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [in terms](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 14, Citation Count: 2

The research presented here takes place in the context of the EC Funded ArchWare project which focuses on innovative architecture-centric languages, frameworks and for engineering evolvable software systems. Of particular interest are complex and ..

Keywords: generative programming, hypercode, structural reflection, system evolution

10 [GPGPU: general purpose computation on graphics hardware](#)

 David Luebke, Mark Harris, Jens Krüger, Tim Purcell, Naga Govindaraju, Ian Buck, Cliff Woolley, Aaron Lefohn

August 2004 **SIGGRAPH '04: SIGGRAPH 2004 Course Notes**


Publisher: ACM

Full text available:  Pdf (63.03 MB) Additional Information: [full citation](#), [abstract](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 229, Downloads (12 Months): 1673, Citation Count: 7


The graphics processor (GPU) on today's commodity video cards has evolved into an extremely powerful and flexible processor. The latest graphics architectures provide tremendous memory bandwidth and computational horsepower, with fully programmable vertex ...

11 [Learning from project history: a case study for software development](#)

 Davor Čubranić, Gail C. Murphy, Janice Singer, Kellogg S. Booth

November 2004 **CSCW '04: Proceedings of the 2004 ACM conference on Computer supported cooperative work**

Publisher: ACM


Full text available:  Pdf (428.12 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [in terms](#)

Bibliometrics: Downloads (6 Weeks): 20, Downloads (12 Months): 134, Citation Count: 5

The lack of lightweight communication channels and other technical and sociological difficulties make it hard for new members of a non-located software development team to learn effectively from their more experienced colleagues while they are coming ...


Keywords: project memory, recommender system, software artifacts, software development teams, user studies

12 [Practicing JUDO: Java under dynamic optimizations](#)

 [Michał Cierniak](#), [Guei-Yuan Lueh](#), [James M. Stichnoth](#)

August 2000 **PLDI '00: Proceedings of the ACM SIGPLAN 2000 conference on Programming language design and implementation**

Publisher: ACM

Full text available:  Pdf (190.06 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [in terms](#)


Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 96, Citation Count: 59

A high-performance implementation of a Java Virtual Machine (JVM) consists of efficient implementation of Just-In-Time (JIT) compilation, exception handling, synchronization mechanism, and garbage collection (GC). These components are tightly coupled ...

Also published in:

May 2000 **SIGPLAN Notices** Volume 35 Issue 5

13 [SoftTest: a framework for software testing of Java programs](#)

 [B. Childers](#), [M. L. Soffa](#), [Jonathan Beaver](#), [L. Ber](#), [K. Cammarata](#), [T. Kane](#), [J. Litman](#), [J. Misurda](#)

October 2003 **eclipse '03: Proceedings of the 2003 OOPSLA workshop on eclipse technology exchange**


Publisher: ACM

Full text available:  Pdf (304.74 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 28, Citation Count: 1


Producing reliable and robust software has become one of the most important software development concerns in recent years. Testing is a process by which software quality can be assured through the collection of information about software. While testing ...

14 [Relative debugging and its application to the development of large numerical models](#)

 [David Abramson](#), [Ian Foster](#), [John Michalakes](#), [Rok Sosic](#)

December 1995 **Supercomputing '95: Proceedings of the 1995 ACM/ IEEE conference on Supercomputing (CDROM) - Volume 00**, Volume 00

Publisher: ACM

Full text available:  Html (39.80 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [in terms](#)

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 14, Citation Count: 1

Because large scientific codes are rarely static objects, developers are often faced with the tedious task of accounting for discrepancies between new and old versions. In this paper we describe a new technique called relative debugging that addresses ...

Keywords: Debugging, Tools, Parallelism, Guard, Scientific Computing, Relative Debugging, MM5, Meteorology

15 [PDB: Pervasive Debugging With Xen](#)

Alex Ho, Steven Hand, Tim Harris

November 2004 **GRID '04**: Proceedings of the 5th IEEE/ACM International Workshop on Computing

Publisher: IEEE Computer Society

Full text available:  Pdf (159.50 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 18, Citation Count: 2

Building distributed grid applications is notoriously difficult: the complex interactions between concurrently running processes, middleware, operating systems, underlying devices, and interconnecting networks can lead to unpredictable and difficult ...

16 [Transformations for model checking distributed Java programs](#)

Scott D. Stoller, Yanhong A. Liu

May 2001 **SPIN '01**: Proceedings of the 8th international SPIN workshop on Model checking of software

Publisher: Springer-Verlag New York, Inc.

Full text available:  Pdf (108.43 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 8, Citation Count: 3


This paper describes three program transformations that extend the scope of model checkers for Java programs to include distributed programs, *i.e.*, multi-process programs. The transformations combine multiple processes into a single process, replace ...

17 [A Service Scheduler in a Trustworthy System](#)

Yinong Chen

April 2004 **ANSS '04**: Proceedings of the 37th annual symposium on Simulation

Publisher: IEEE Computer Society

Full text available:  Pdf (278.85 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [in terms](#)

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 9, Citation Count: 1

The aim of the research is to investigate techniques that support efficient service scheduling algorithms in a service-oriented fault-tolerant real-time distributed system. Techniques we developed include deadline-based real-time scheduling, priority-based ...

Keywords: Scheduling algorithm, resource allocation, distributed system, fault-tolerant system


18 [A component-based approach to modeling and simulating mixed-signal and hybrid systems](#)



Jie Liu, Edward A. Lee

October 2002 **Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 10, Issue 4

Publisher: ACM

Full text available:  Pdf (1.07 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [in terms](#)


Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 80, Citation Count: 1

Systems with both continuous and discrete behaviors can be modeled using a mixed-signal style or a hybrid systems style. This article presents a component-based mode


and simulation framework that supports both modeling styles. The component frame
...

Keywords: Component-based modeling, Ptolemy II, actors-oriented design, hierarch heterogeneity, hybrid systems, mixed-signal systems, simulation

19 [Process migration](#)

 [Dejan S. Milošević, Fred Douglass, Yves Paindaveine, Richard Wheeler, Songnian Zhou](#)
September 2000 **Computing Surveys (CSUR)** , Volume 32 Issue 3

Publisher: ACM


Full text available:  [Pdf](#) (1.24 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [in terms](#), [review](#)

Bibliometrics: Downloads (6 Weeks): 110, Downloads (12 Months): 680, Citation Count: 35


Process migration is the act of transferring a process between two machines. It enables dynamic load distribution, fault resilience, eased system administration, and data access locality. Despite these goals and ongoing research efforts, migration has ...

Keywords: distributed operating systems, distributed systems, load distribution, process migration

20 [Adventures in interoperability: the SML.NET experience](#)

 [Nick Benton, Andrew Kennedy, Claudio V. Russo](#)
August 2004 **PPDP '04: Proceedings of the 6th ACM SIGPLAN international conference on Principles and practice of declarative programming**

Publisher: ACM

Full text available:  [Pdf](#) (434.04 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [in terms](#), [review](#)

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 23, Citation Count: 9

SML.NET is a compiler for Standard ML that targets the Common Language Runtime .NET is integrated into the Visual Studio development environment. It supports easy interoperability with other .NET languages via a number of language extensions, which go ...

Keywords: applications of declarative programming, functional programming, integration of paradigms, programming environments

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